

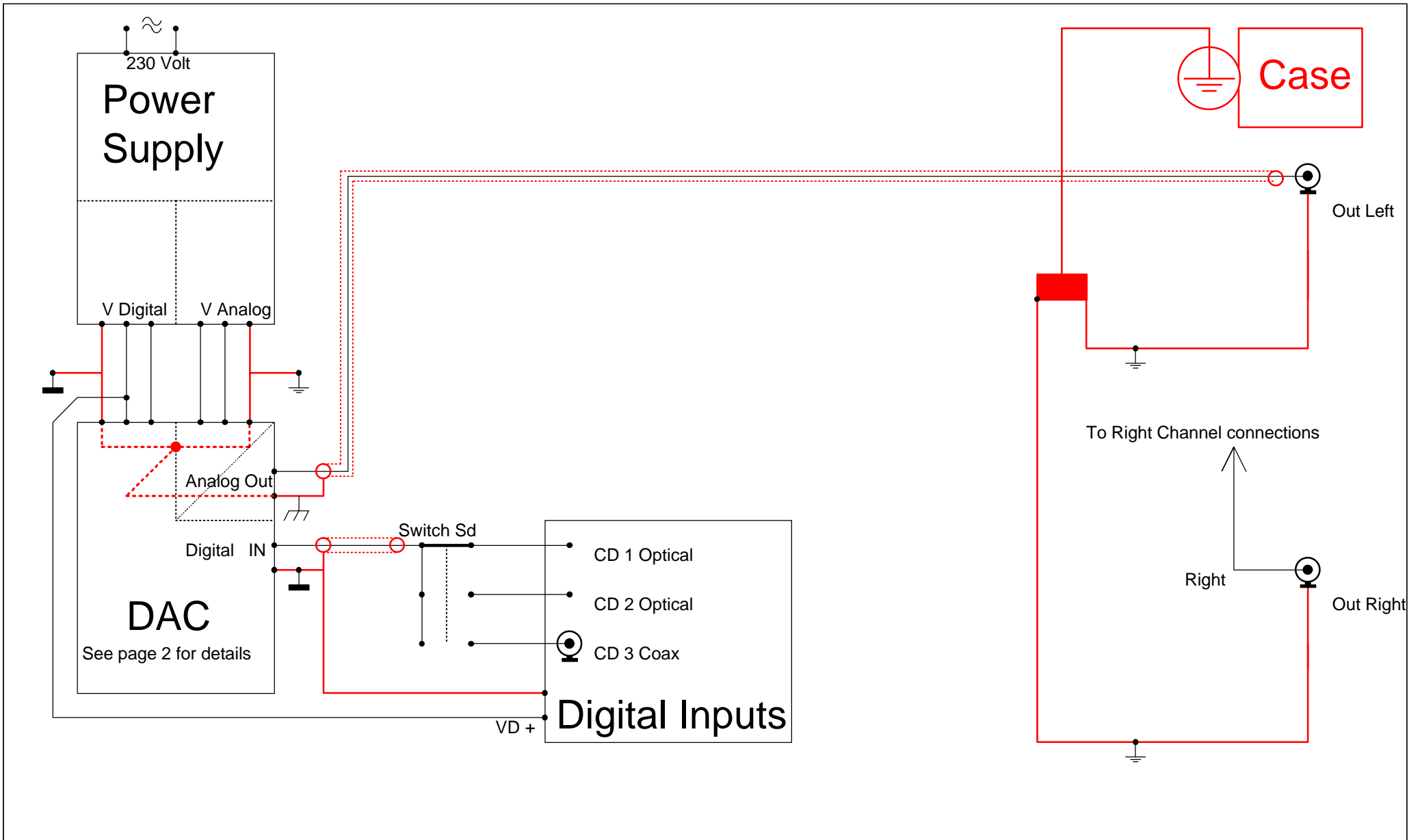
DDDAC 2000



Digital to Analog Converter

By Doede Douma

Version 3.1 Updated November 21, 2001

www.dddac.de

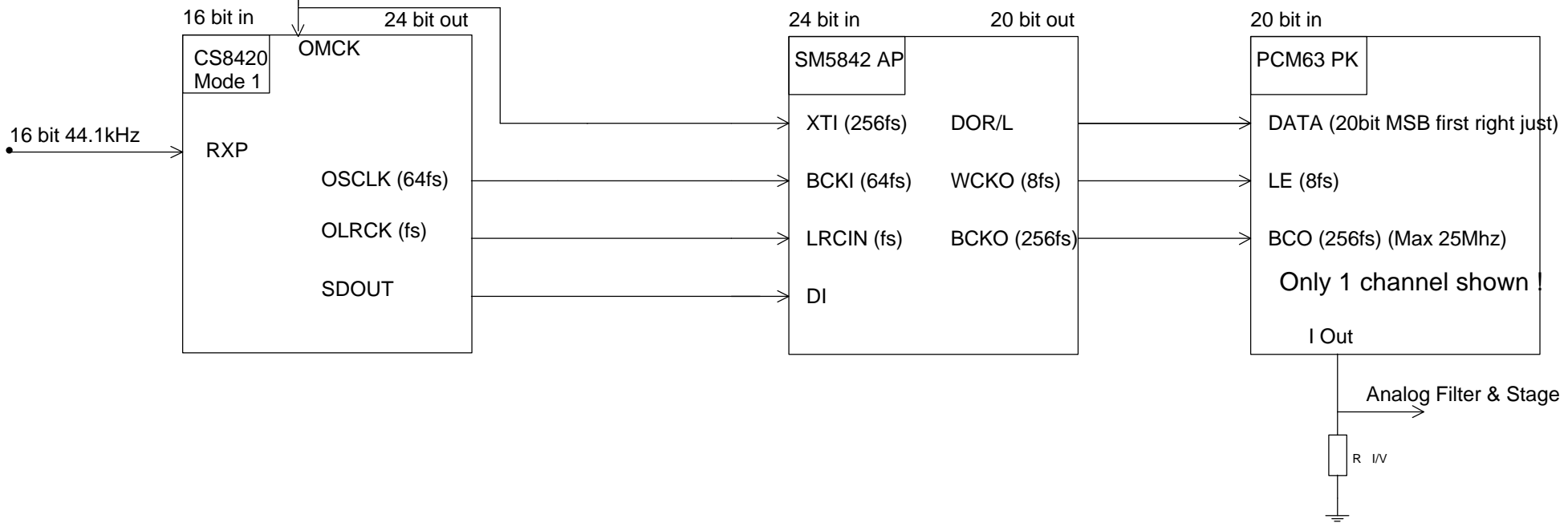


	Analog Ground DAC
	Digital Ground DAC

DDDAC 2001, Block Diagram	
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XClock Oscillator
 256 x fs
 fs = 44.1 - 50 kHz

fs	8 fs freq	64 fs freq	256 fs freq
44.1 kHz	352,8Hz	2.8224 MHz	11.2896 MHz
50.78125 kHz	406.25 kHz	3.2500 MHz	13.0000 MHz



At Clockspeeds of 256fs clocked data is 32 bit wide

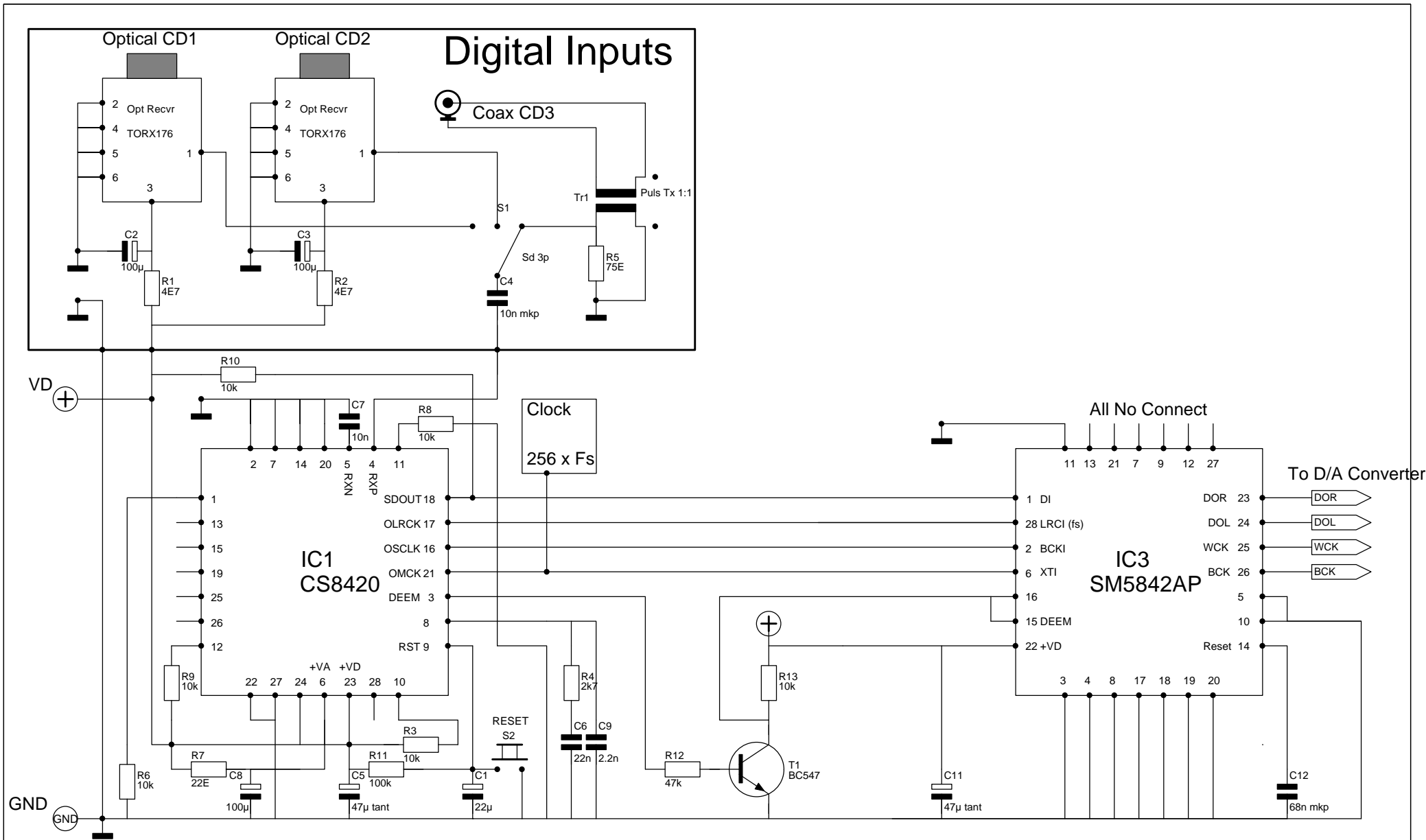
DDDAC 2001, DAC Basics

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S2: RESET

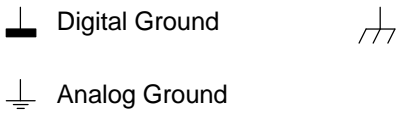
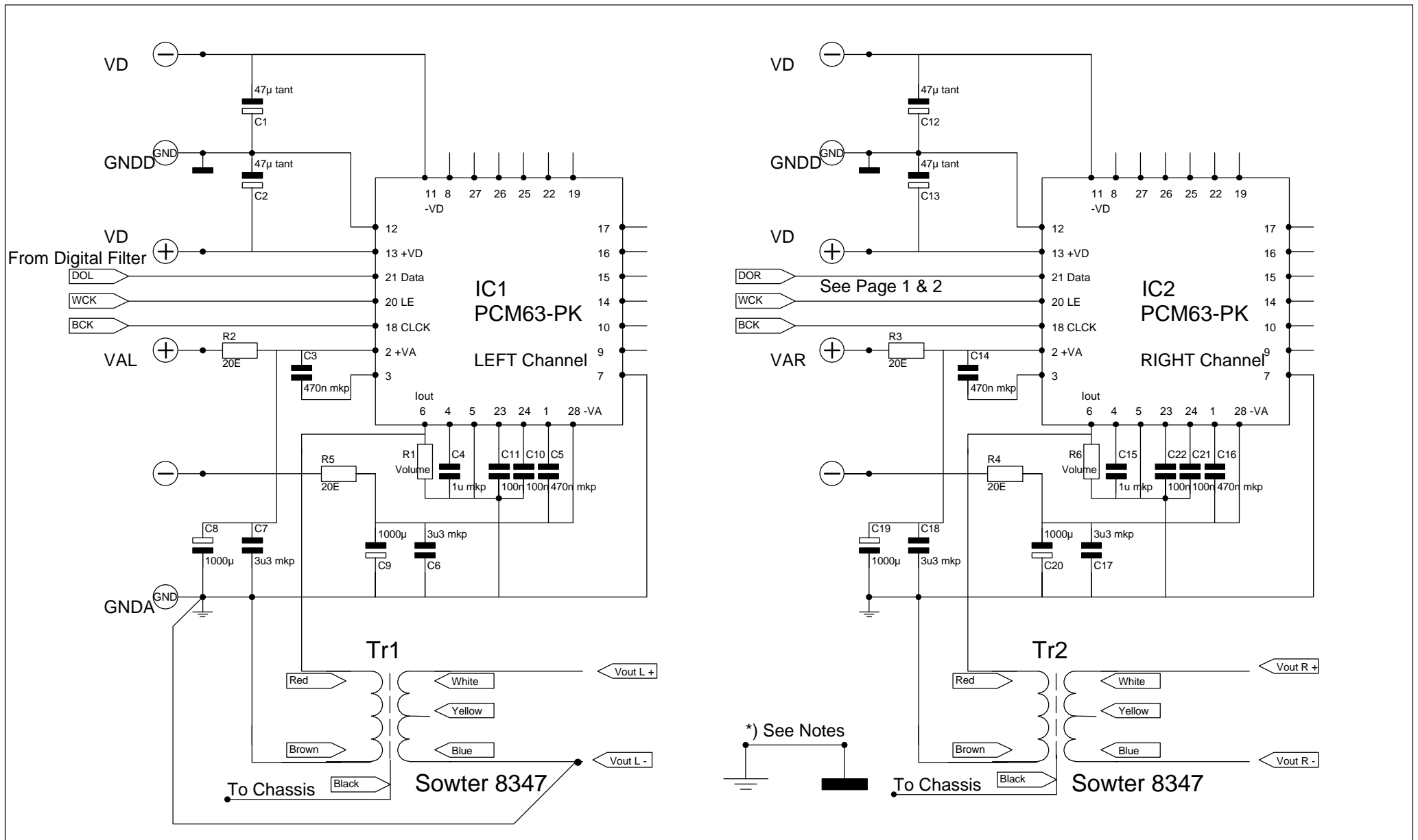
DDDAC 2001, Digital Filter

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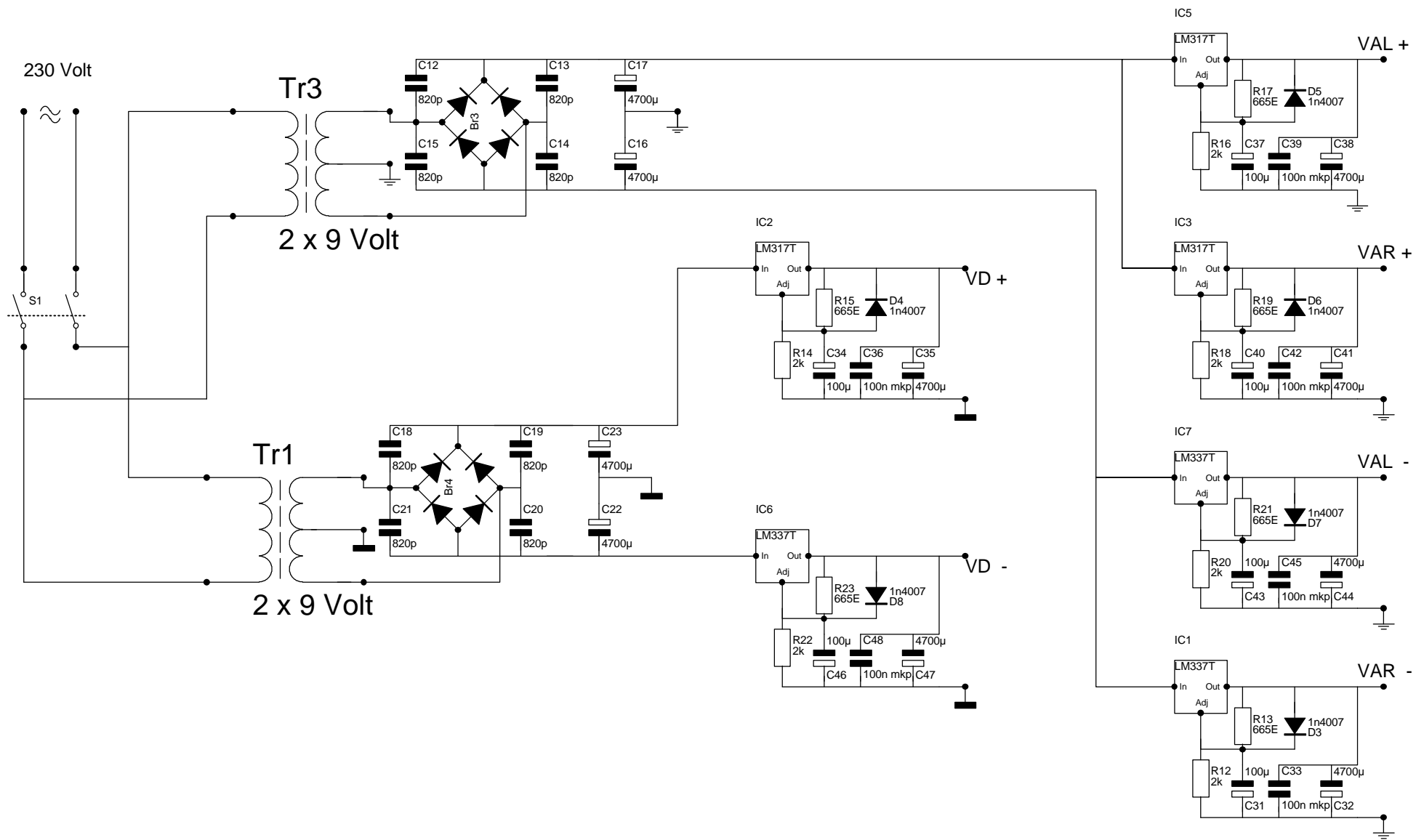
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DDDAC 2001, DAC Stage

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*) Connect both grounds at one point close to PCM63P

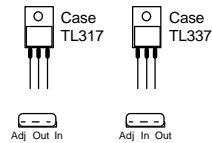


Digital Ground DAC

VD/VA: 5,0 Volt



Analog Ground DAC



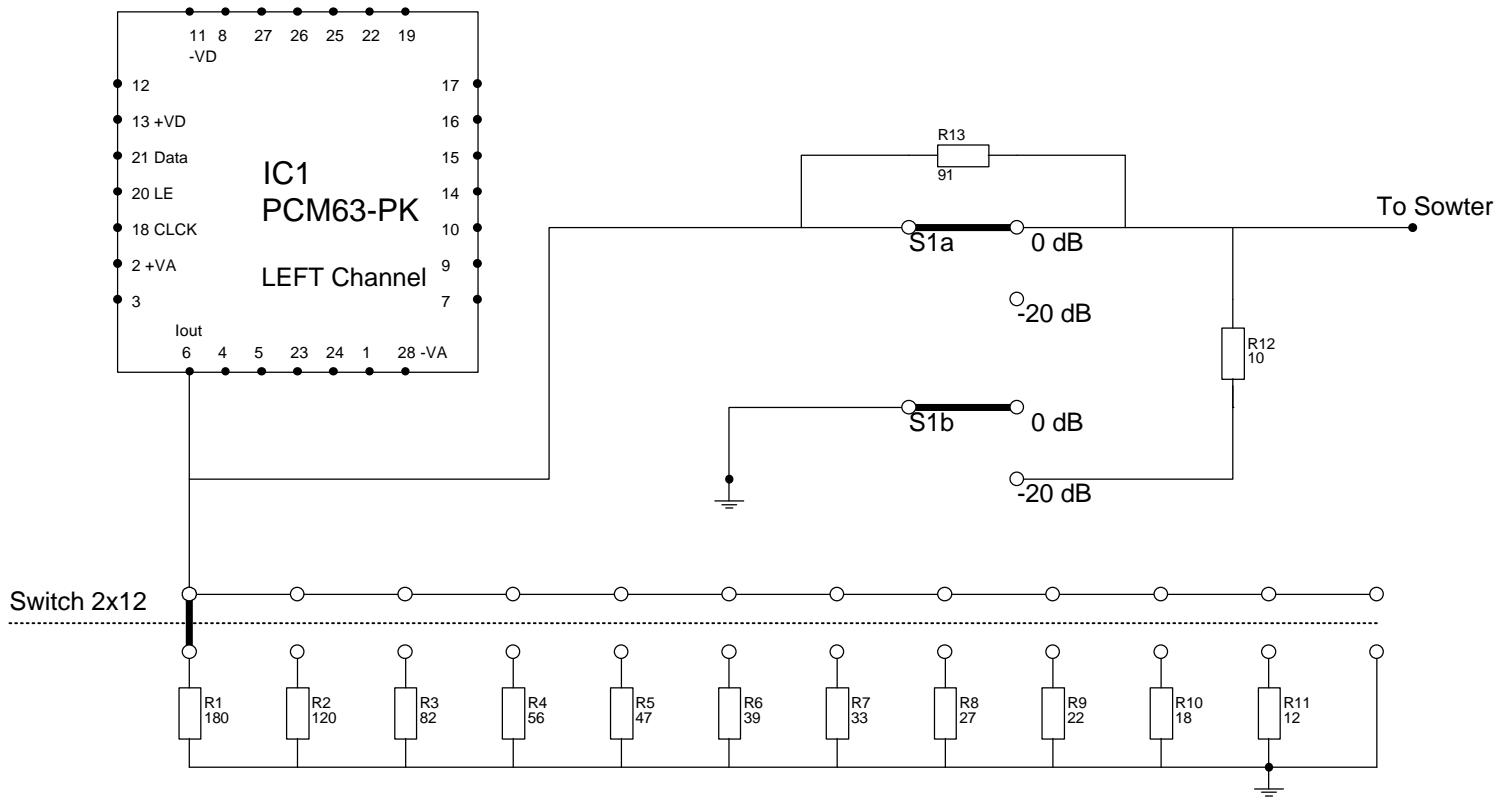
DDDAC 2001, Power Supply

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DDDAC 2001, Volume Control

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